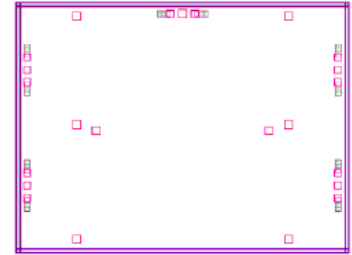


**AMT1701**  
**4 Channels 28-37GHz Vector Modulation Chip**



**Key Features :**

- Frequency range : 28 – 37GHz
- Type : single balanced
- Phase modulation range : 360°
- Amplitude modulation range : -30dB ~ -16dB
- Output standing wave : 1.9
- Input standing wave : 1.4
- Voltage bias : -2V ~ 0V
- Chip dimensions : 3.9mm x 2.9mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

**Description :**

AMT1701 chip is a 4 channels output single balance analog modulation device, it realizes signal amplitude and phase joint modulation. It is designed by 0.25μm Gallium Arsenide (GaAs) pHEMT process. This chip is designed with ground through metal vias on the back technology. All chip products p are 100% RF tested.

**Absolute Maximum Ratings (Ta = 25°C)**

Symbol	Parameter	Value	Remark
Vi, Vq	Control Voltage	0.6V/-5V	
Pin	Input Power	20dBm	
Tm	Sintering Temperature	310°C	30s, N <sub>2</sub> protection
Tstg	Storage Temperature	-65 ~ +150°C	

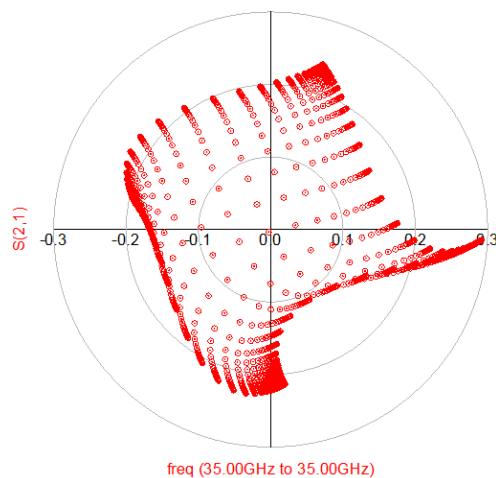
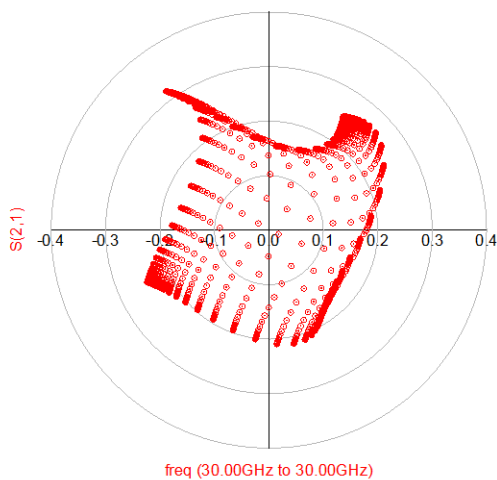
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

**Electrical Characteristics (Ta = 25°C)**

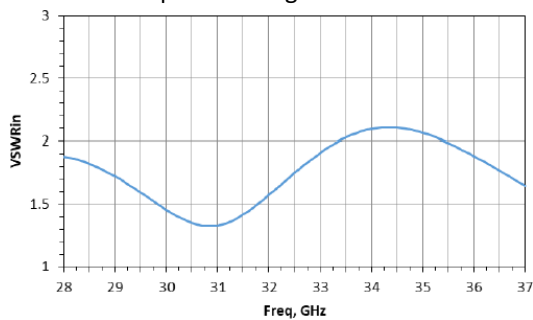
Symbol	Parameter	Value			Unit
		Min	Typical	Max	
VSWRin	Input Standing Wave	-	1.4	1.5	
VSWRout	Output Standing Wave	-	1.9	2.2	
IL	Insertion Loss	-	12	13	dB

## Typical Performance

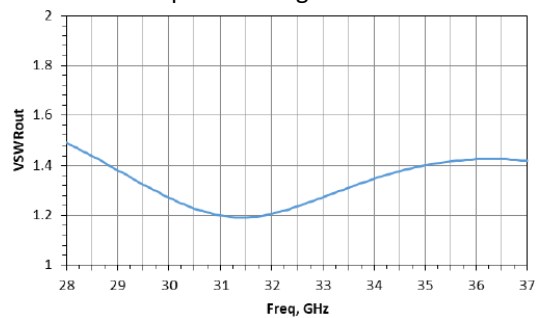
Amplitude Phase Modulation Curve



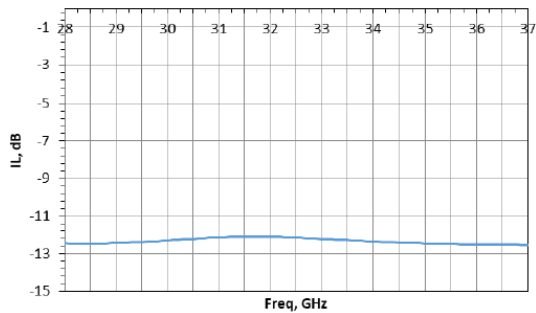
Input Standing Wave Curve



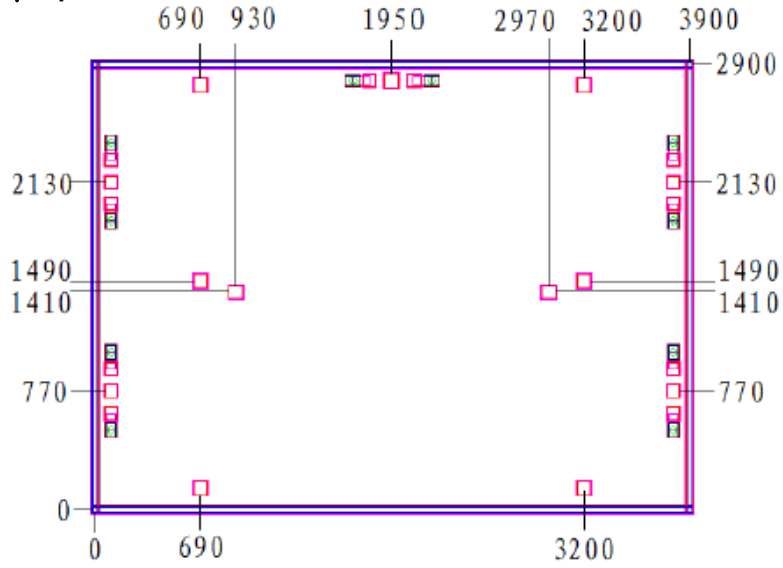
Output Standing Wave Curve



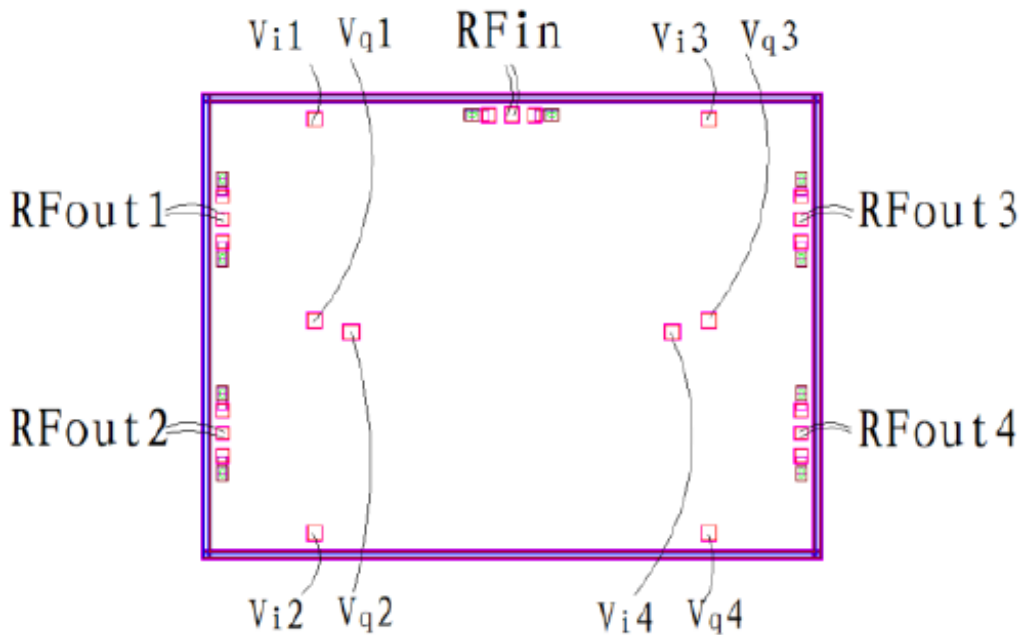
Insertion Loss Curve



**Chip Dimensions (Unit :  $\mu\text{m}$ )**



**Chip Layout Diagram**



**Pad Definition**

No.	Symbol	Function	PAD dimension	Explanation
1	RFin	Input point	100*100 $\mu\text{m}^2$	External connect to 50 $\Omega$ system
2	RFout	Output point	90*90 $\mu\text{m}^2$	External connect to 50 $\Omega$ system
3	Vi1/2/3/4	Control point	100*100 $\mu\text{m}^2$	Recommend operation voltage -2V ~ 0V
4	Vq1/2/3/4	Control point	100*100 $\mu\text{m}^2$	Recommend operation voltage -2V ~ 0V

Please see Appendix A for details.