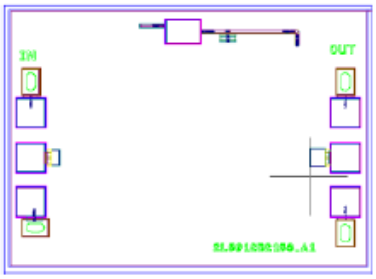
AMT1203
2 – 8GHz Low Noise Amplifier Chip



Key Features :

- Frequency range : 2 – 8GHz
- Typical gain : 27dB
- Input/output standing wave : 1.4/1.3
- Noise figure : 0.7dB
- P-1 : 11dBm @ +5V/30mA
- Chip dimensions : 2.2mm x 1.15mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

Description :

AMT1203 chip is a Gallium Arsenide (GaAs) high performance Low Noise Amplifier, it covers 2 - 8GHz frequency range. AMLA0009S uses +5V single voltage operation, Noise Figure is 0.7dB, and 27dB typical gain. This chip is designed with ground through metal vias on the back technology. All chip products are 100% RF tested.

Absolute Maximum Ratings (Ta = 25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	+7V	
Pin	Input Signal Power	17dBm	
Tch	Operating Temperature	150°C	
Tm	Sintering Temperature	310°C	30s, N_2 protection
Tstg	Storage Temperature	-65 ~ +150°C	

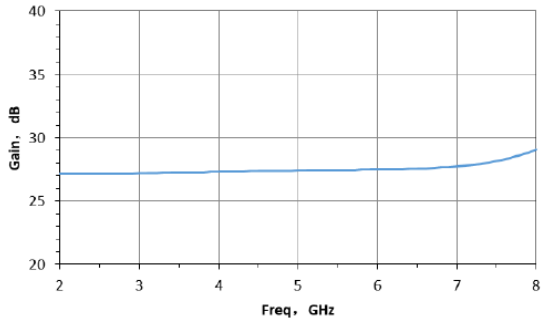
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

Electrical Characteristics (Ta = 25°C)

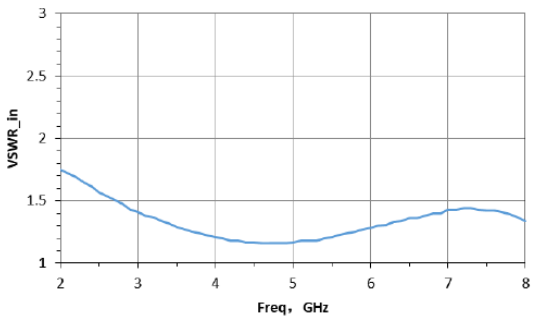
Symbol	Parameter	Test Conditions	Value			Unit
			Min	Typical	Max	
G	Gain	Vd = +5V F : 2 ~ 8GHz	-	27	-	dB
NF	Noise Figure		-	0.7	0.9	dB
Id	Drain Current		-	30	-	mA
VSWR_in	Input VSWR		-	1.4	1.8	-
VSWR_out	Output VSWR		-	1.3	1.5	-
P-1	Output Compression at 1dB point		10	11	-	dBm

Typical Performance

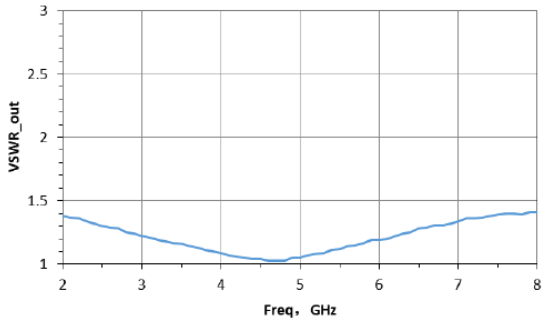
Gain Curve



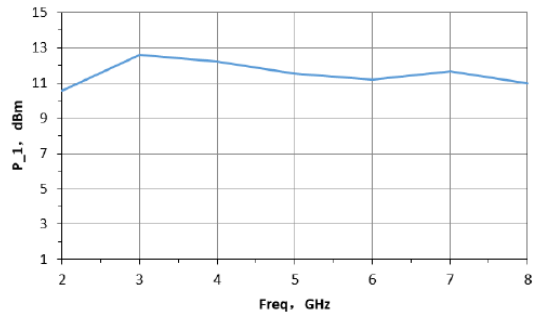
Input Standing Wave Curve



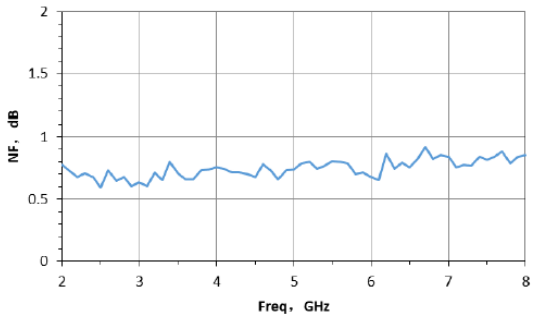
Output Standing Wave Curve



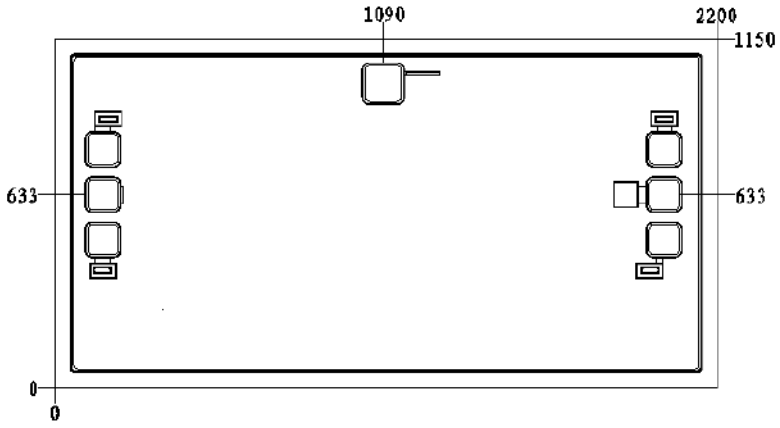
P-1 Compression Characteristic Curve



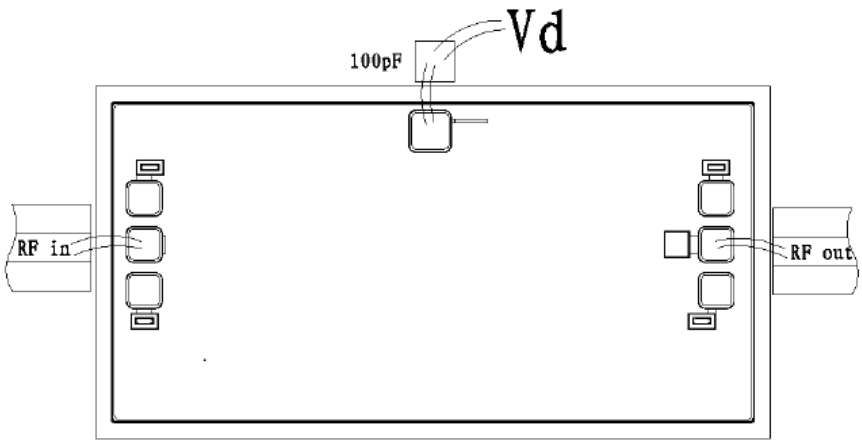
Noise Figure Curve



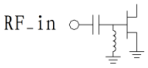
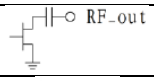
Chip Dimensions (Unit : μm)



Chip Layout Diagram



Pad Definition

Symbol	Function	Dimension	Equivalent Circuit
RF_in	RF signal input port, connecting to external 50Ω system. no need to add DC blocking capacitor.	100*100μm^2	
RF_out	RF signal output port, connecting to external 50Ω system, no need to add DC blocking capacitor.	100*100μm^2	
Vd	Amplifier bias, need to connect external 100pF capacitor.	100*100μm^2	

Please see Appendix A for details.