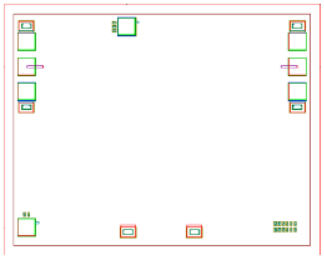
AMT1622

0.4 – 6GHz Voltage Controlled Attenuator Chip



Key Features :

- Frequency range : 0.4 – 6GHz
- Insertion loss : 2.5dB
- Attenuation range : 2.5 – 30dB
- Input/output standing wave : 1.6/1.6
- Chip dimensions : 1.9mm x 1.5mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

Description :

AMT1622 is a high performance fixed attenuator chip, it is designed by Gallium Arsenide (GaAs) pHEMT process. It covers frequency range of 0.4 – 6GHz, typical insertion loss is 2.5dB, attenuation range is 2.5 - 30dB. This chip is designed with ground through metal vias on the back technology. All chip products p are 100% RF tested. This chip is for microwave transceiver module, to realize transceiver signal amplitude control function.

Absolute Maximum Ratings (Ta = 25°C)

Symbol	Parameter	Value	Remark
Pin	Max. input signal power	+25dBm	
VC	Control voltage	-0.5V ~ 7V	
Tch	Operation Temperature	-55 ~ 125°C	
Tstg	Storage Temperature	-65 ~ 150°C	

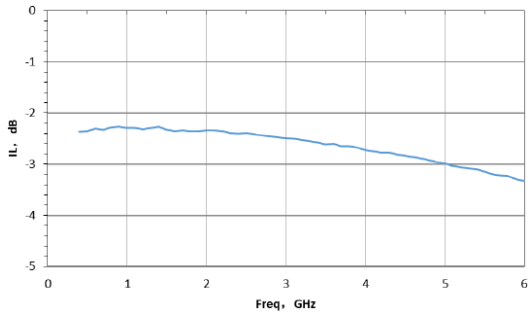
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

Electrical Characteristics (Ta = 25°C)

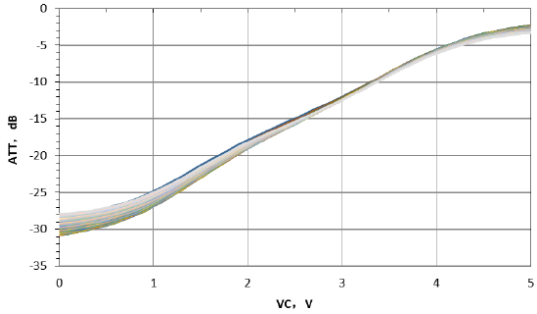
Symbol	Parameter	Value			Unit
		Min	Typical	Max	
F	Frequency range		0.4 - 6		GHz
IL	Insertion Loss	-	2.5	3.5	dB
ATT	Attenuation range		2.5 - 30		dB
VSWRin	Input Standing Wave	-	1.6	1.9	-
VSWRout	Output Standing Wave	-	1.6	1.9	-

Typical Performance

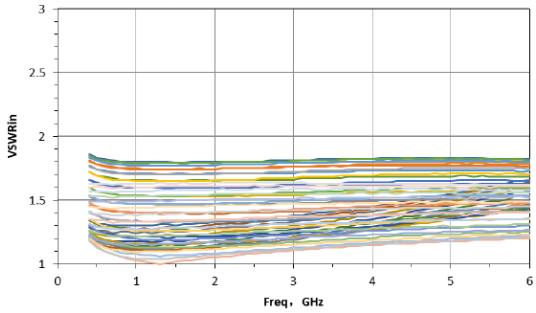
Insertion Loss Curve



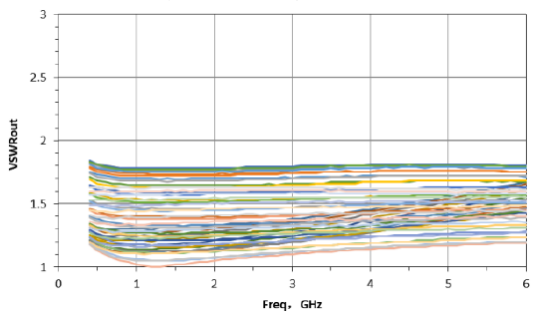
Attenuation Range Curve



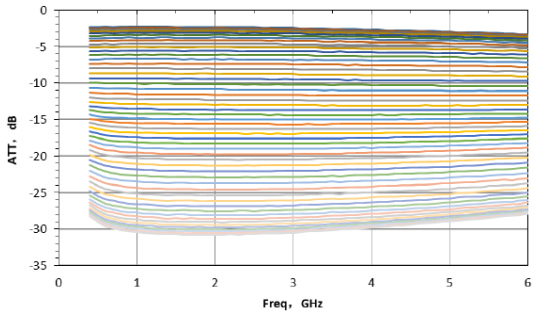
Input Standing Wave Curve



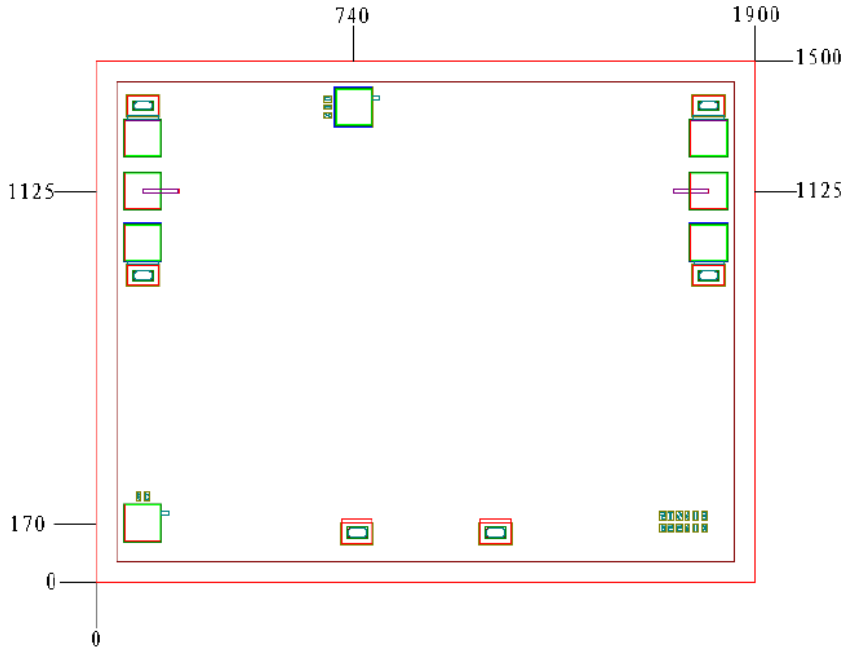
Output Standing Wave Curve



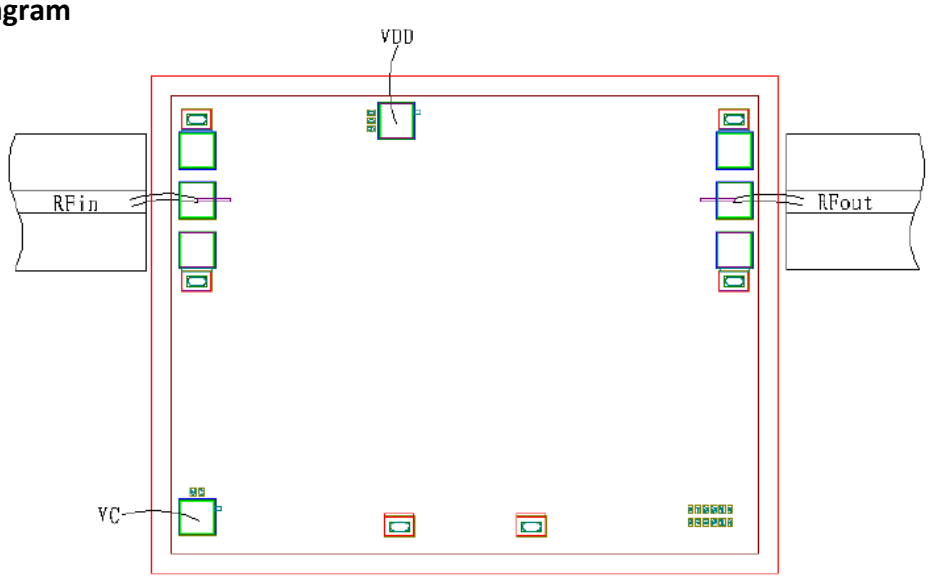
Each Attenuation Curve



Chip Dimensions (Unit : μm)



Chip Layout Diagram



Pad Definition

No.	Symbol	Function	Dimension
1	RFin	RF signal input port, external connect to 50Ω system, internal built in DC blocking capacitor	100μm*100μm
2	RFout	RF signal output port, external connect to 50Ω system, internal build in DC blocking capacitor	100μm*100μm
3	VDD	+5V supply voltage	100μm*100μm
4	VC	Control port, 0 - +5V, 0V is max. attenuation, +5V is initial state	100μm*100μm

Please see Appendix A for details.