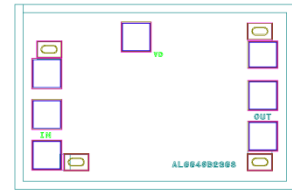


**AMT1235**  
**12 – 18GHz Low Noise Amplifier Chip**

**Key Features :**

- Frequency range : 12 – 18GHz
- Typical gain : 25dB @ 30mA
- Input standing wave : 1.3
- Output standing wave : 1.3
- Noise figure : 1.0dB
- P-1 : 12dBm @ +5V/30mA
- Chip dimensions : 1.00mm x 0.65mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.



**Description :**

AMT1235 chip is a Gallium Arsenide (GaAs) high performance Low Noise Amplifier, it covers 12 – 18GHz frequency range. It uses +5V single voltage operation, noise figure is 1.0dB, and 25dB typical gain. This chip is designed with ground through metal vias on the back technology.

**Absolute Maximum Ratings (Ta = 25°C)**

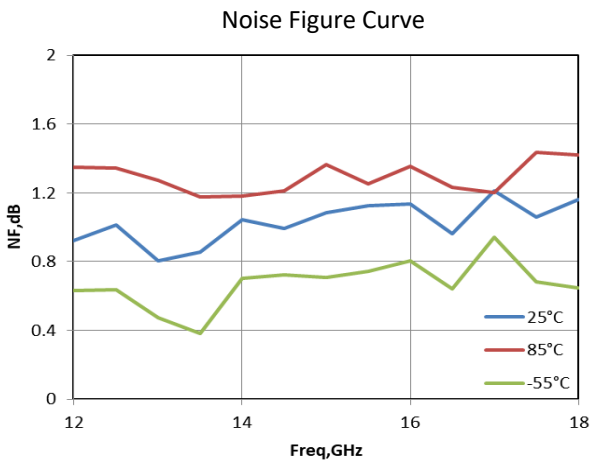
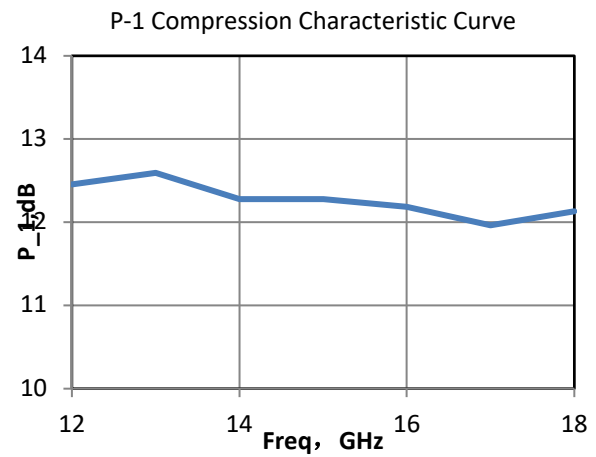
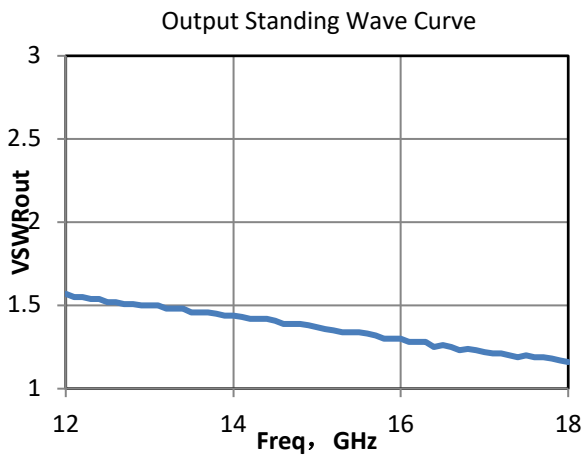
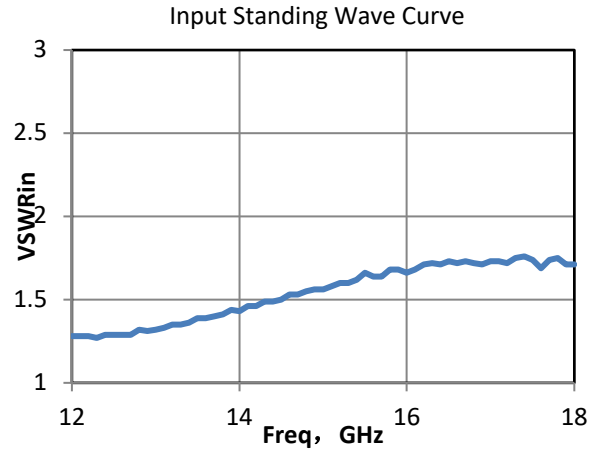
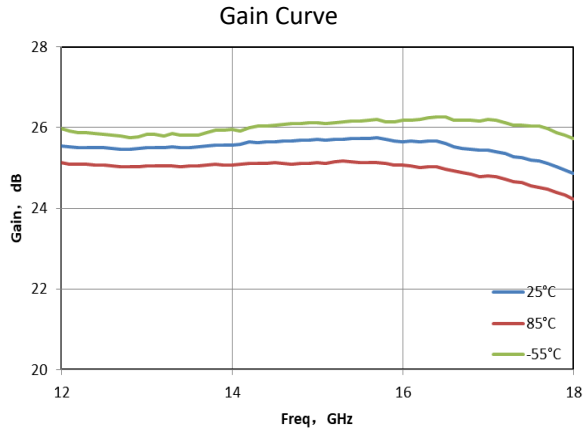
Symbol	Parameter	Value	Remark
Vd	Drain Voltage	+7V	
Pin	Input Signal Power	17dBm	
Tch	Operating Temperature	150°C	
Tm	Sintering Temperature	310°C	30s, N <sub>2</sub> protection
Tstg	Storage Temperature	-65 ~ +150°C	

[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

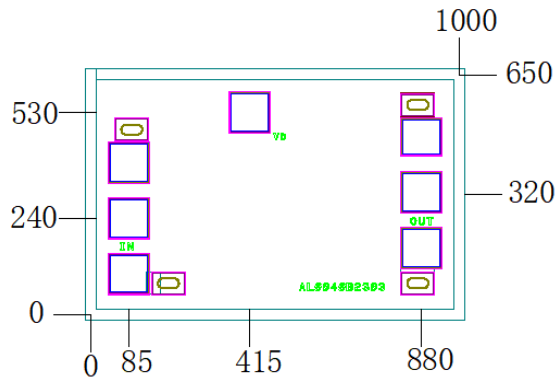
**Electrical Characteristics (Ta = 25°C)**

Symbol	Parameter	Test Conditions	Value			Unit
			Min	Typical	Max	
G	Gain	Vd = +5V F : 12 ~ 18GHz	-	25	-	dB
NF	Noise Figure		-	1.0	-	dB
Id	Static Current		-	30	-	mA
VSWR_in	Input Standing Wave		-	1.3	1.8	-
VSWR_out	Output Standing Wave		-	1.3	1.6	-
P-1	Output Power at 1dB point		-	12	-	dBm

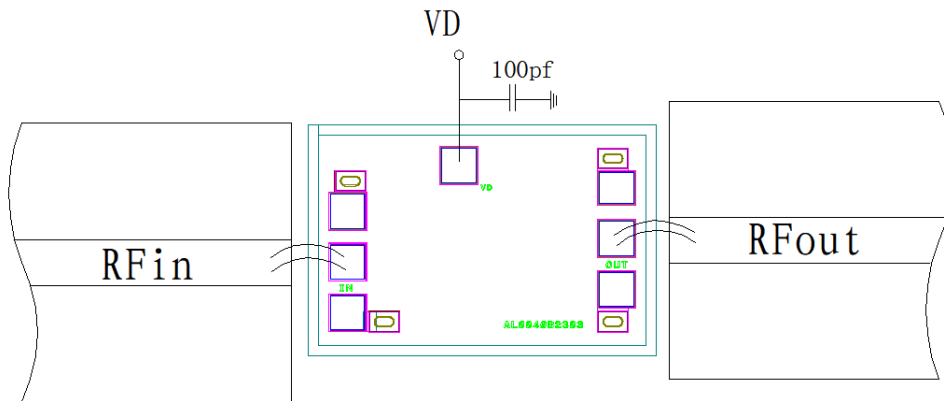
Typical Performance



**Chip Dimensions (Unit :  $\mu\text{m}$ )**



**Chip Layout Diagram**



**Pad Definition**

Symbol	Function Description	Dimensions	Equivalent Circuit
RFin	RF signal input port, connecting to external 50 $\Omega$ system, no need to add DC blocking capacitor.	100 $\mu\text{m}$ *100 $\mu\text{m}$	
RFout	RF signal output port, connecting to external 50 $\Omega$ system, no need to add DC blocking capacitor.	100 $\mu\text{m}$ *100 $\mu\text{m}$	
VD	Amplifier bias, need to connect 100pF external capacitor	100 $\mu\text{m}$ *100 $\mu\text{m}$	

Please see Appendix A for details.