## AMT1105 8 – 12GHz Power Amplifier Chip



#### **Key Features:**

Frequency range: 8 – 12GHz
Typical small signal Gain: 20dB
Typical output power: 23dBm

Voltage bias: 8V, 0.11A

• Chip dimensions: 1.5mm x 2.0mm x 0.1mm

• Applications: wireless communication, transceiver module, radio telecommunication etc.

#### **Description:**

AMT1105 chip is a Gallium Arsenide (GaAs) designed power amplifier chip, with a frequency range of 8 – 12GHz, single voltage operation, drain voltage Vds at 8V, linear gain of 20dB, saturated output power of 23dBm. This chip is designed with ground through metal vias on the back technology. All chip products p are 100% RF tested.

## **Absolute Maximum Ratings (Ta = 25°C)**

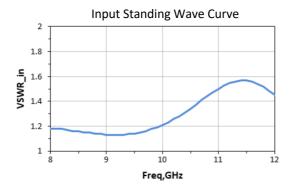
| <u> </u> |                       |              |                                |  |  |  |
|----------|-----------------------|--------------|--------------------------------|--|--|--|
| Symbol   | Parameter             | Value        | Remark                         |  |  |  |
| Vd       | Drain Voltage         | +11V         |                                |  |  |  |
| Pin      | Input Signal Power    | 15dBm        |                                |  |  |  |
| Tch      | Operating Temperature | -55 ~ +125°C |                                |  |  |  |
| Tm       | Sintering Temperature | 310°C        | 30s, N <sub>2</sub> protection |  |  |  |
| Tstg     | Storage Temperature   | -65 ~ +150°C |                                |  |  |  |

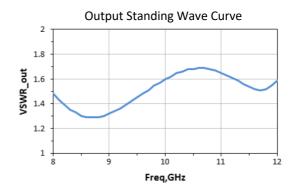
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

#### **Electrical Characteristics (Ta = 25°C)**

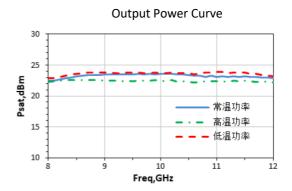
| Symbol   | Parameter              | <b>Test Condition</b> | Value |         |     | Unit |
|----------|------------------------|-----------------------|-------|---------|-----|------|
|          |                        |                       | Min   | Typical | Max |      |
| G        | Small Signal Gain      |                       | 19    | 20      | 21  | dB   |
| Id       | Operating Current      | Vd = 8V               | -     | 110     | •   | mA   |
| VSWR_in  | Input SW               | F:8~12GHz             | -     | 1.6     | -   |      |
| VSWR_out | Output SW              |                       |       | 1.5     |     |      |
| Gp       | Power Gain             | Vd = 8V               |       | 18      | -   | dB   |
| Po(sat)  | Saturated Output Power | F:8~12GHz             | -     | 23      | -   | dBm  |
|          |                        | Duty Cycle : 20%      |       |         |     |      |

# **Typical Performance**

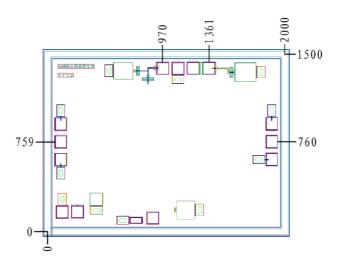




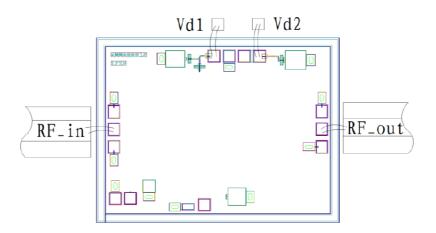




# Chip Dimensions (Unit: $\mu$ m)



## **Chip Layout Diagram**



### **Pad Definition**

| Symbol | Function  | Dimension              | <b>Equivalent Circuit</b>                   |
|--------|---|------------------------|---|
| RF_in  | RF signal input port, connecting to external $50\Omega$ system. DC blocking capacitor is not needed, if external DC current is applied to this pad. | 95*105μm²              | RF-in                                       |
| RF_out | RF signal output port, connecting to external $50\Omega$ system, no need to add DC blocking capacitor.  | 95*105μm²              | RF_out                                      |
| Vd1    | Amplifier drain bias, need external 100pF, 1000pF capacitor.  | 100*100μm <sup>2</sup> | Vd1<br>———————————————————————————————————— |
| Vd2    | Amplifier drain bias, need external 100pF, 1000pF capacitor.  | 100*100μm²             | Vd2   |

Refer to Appendix A for details.