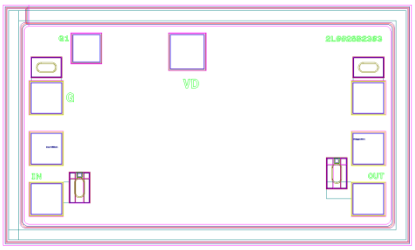
AMT1233
12 – 18GHz Low Noise Amplifier Chip

Key Features :



- Frequency range : 12 – 18GHz
- Typical gain : 22.5dB @ 17mA
  19.5dB @ 9mA
- Input standing wave : 1.2
- Output standing wave : 1.3
- Noise figure : 1.1dB
- P-1 : 9dBm @ +5V/17mA
  2dBm @ +5V/9mA (Low power mode)
- Chip dimensions : 1.15mm x 0.65mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

Description :

AMT1233 chip is a Gallium Arsenide (GaAs) high performance Low Noise Amplifier, it covers 12 – 18GHz frequency range. It uses +5V single voltage operation, noise figure is 1.1dB, and 22.5dB typical gain. This chip is designed with ground through metal vias on the back technology.

Absolute Maximum Ratings (Ta = 25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	+7V	
Pin	Input Signal Power	17dBm	
Tch	Operating Temperature	150°C	
Tm	Sintering Temperature	310°C	30s, N_2 protection
Tstg	Storage Temperature	-65 ~ +150°C	

[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

Electrical Characteristics (Ta = 25°C)

Symbol	Parameter	Test Conditions	Value			Unit
			Min	Typical	Max	
G	Gain	Vd = +5V F : 12 ~ 18GHz	-	22.5	-	dB
NF	Noise Figure		-	1.1	-	dB
Id	Static Current		-	17	-	mA
VSWR_in	Input Standing Wave		-	1.2	-	-
VSWR_out	Output Standing Wave		-	1.3	-	-
P-1	Output Power at 1dB point		-	9	-	dBm

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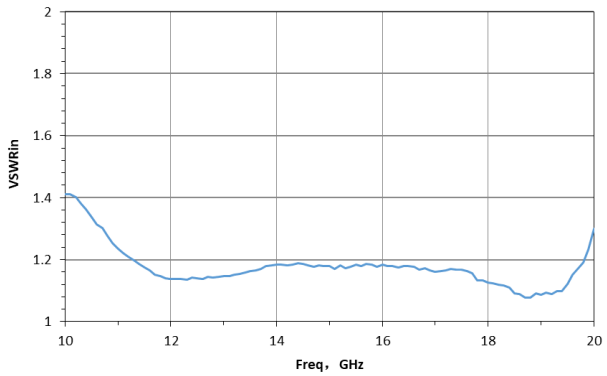
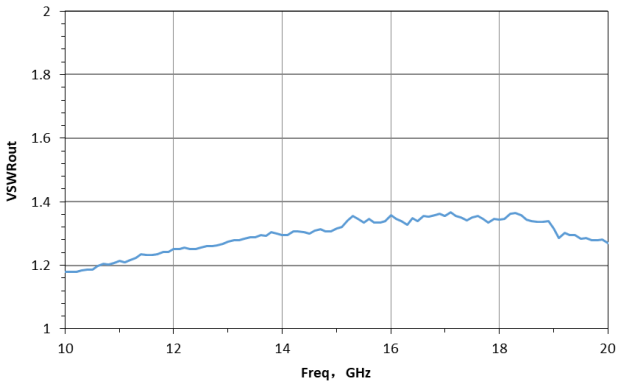
Typical Performance

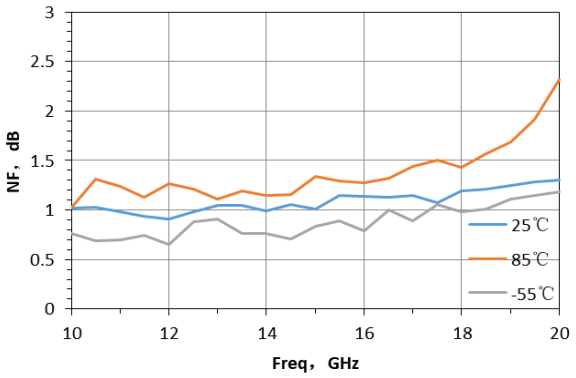
Input Standing Wave Curve



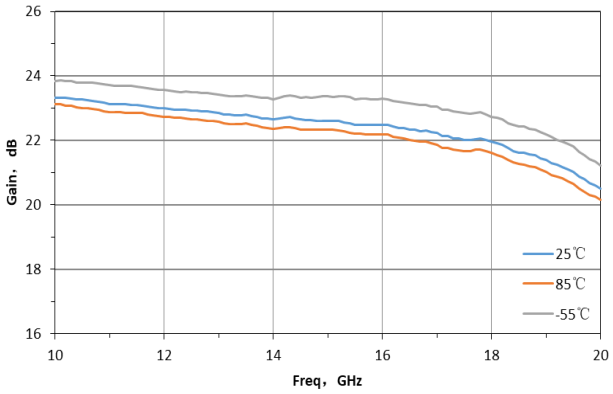
Output Standing Wave Curve



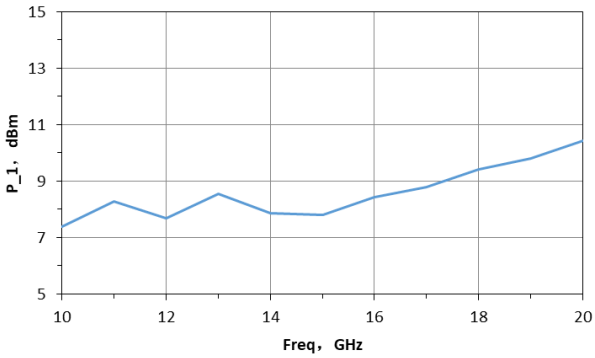
Noise Figure Curve



Gain Curve



P-1 Compression Characteristic Curve



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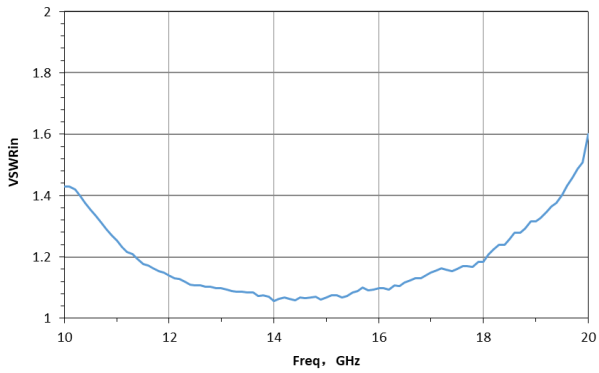
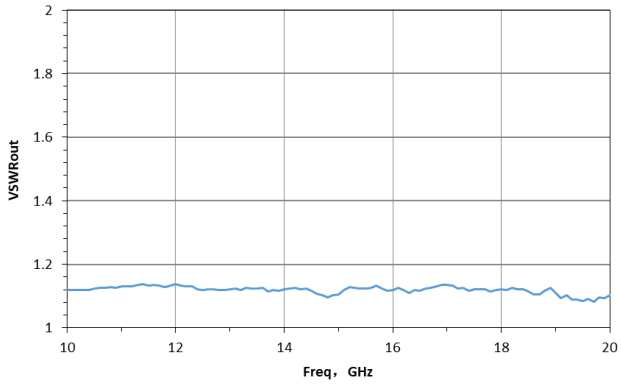
Typical Performance (Low Power Mode)

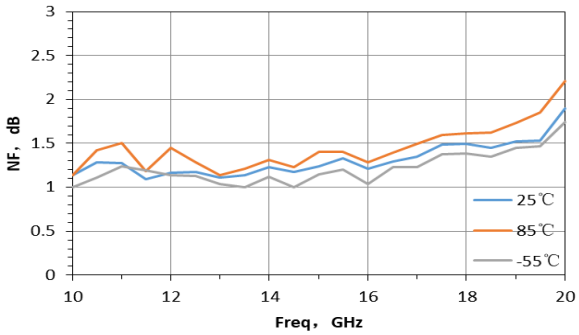
Input Standing Wave Curve



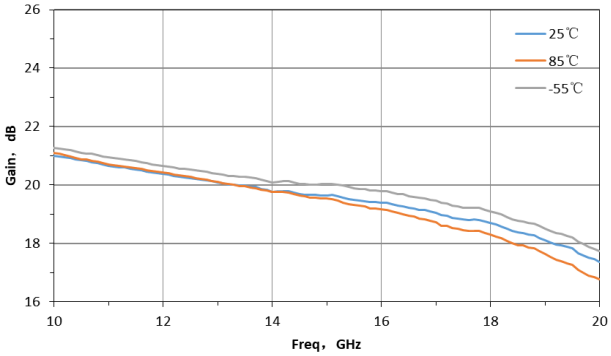
Output Standing Wave Curve



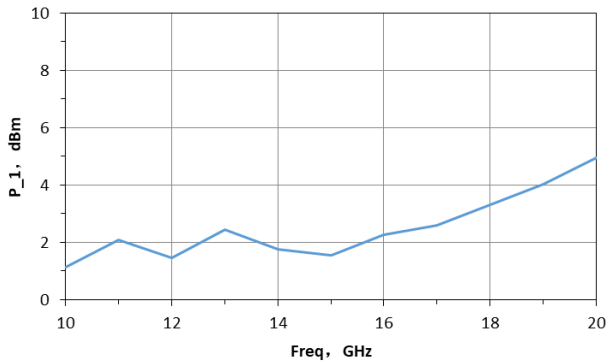
Noise Figure Curve



Gain Curve



P-1 Compression Characteristic Curve



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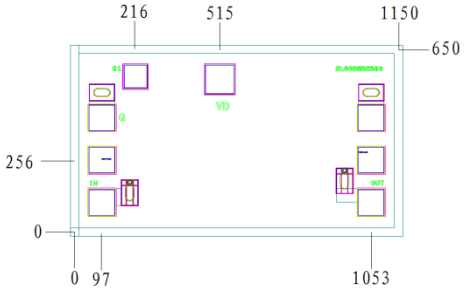
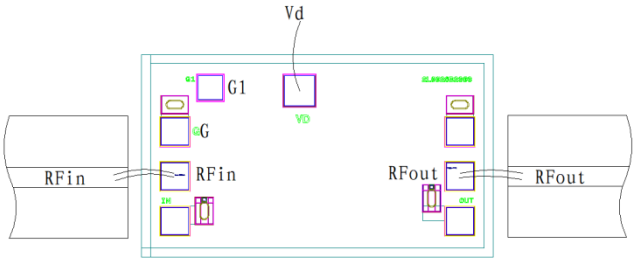
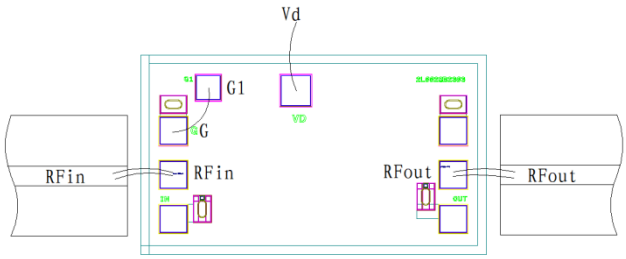
Chip Dimensions (Unit : μm)



Chip Layout Diagram

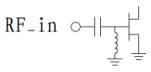
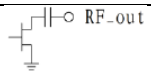


Normal Mode



Low Power Mode (G1 connected to G)

Pad Definition

Symbol	Function Description	Demensions	Equivalent Circuit
RFin	RF signal input port, connecting to external 50Ω system, no need to add DC blocking capacitor.	100μm*100μm	
RFout	RF signal output port, connecting to external 50Ω system, no need to add DC blocking capacitor.	100μm*100μm	
Vd	Amplifier bias, need to connect 100pF external capacitor	100μm*100μm	
G	Ground	100μm*100μm	-
G1	Low power mode when connected to G pad	80μm*80μm	-

Please see Appendix A for details.

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