AMT1215 12 – 20GHz Low Noise Amplifier Chip



Key Features:

Frequency range: 12 – 20GHz
Typical gain: 29dB (positive slope)
Input/output standing wave: 1.5

Noise figure : 1.5dBP-1 : 8dBm @ +5V/65mA

Chip dimensions: 2.25mm x 1.2mm x 0.1mm

Applications: wireless communication, transceiver module, radio telecommunication etc.

Description:

AMT1215 chip is a Gallium Arsenide (GaAs) high performance Low Noise Amplifier, it covers 12 – 20GHz frequency range. It uses +5V single voltage operation, noise figure is 1.5dB, and 29dB typical gain. This chip is designed with ground through metal vias on the back technology. All chip products p are 100% RF tested.

Absolute Maximum Ratings (Ta = 25°C)

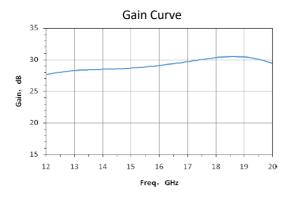
Symbol	Parameter	Value	Remark	
Vd	Drain Voltage	+7V		
Pin	Input Signal Power	17dBm		
Tch	Operating Temperature	150°C		
Tm	Sintering Temperature	310°C	30s, N ₂ protection	
Tstg	Storage Temperature	-65 ~ +150°C		

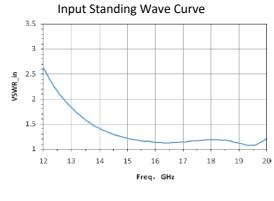
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

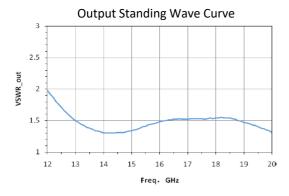
Electrical Characteristics (Ta = 25°C)

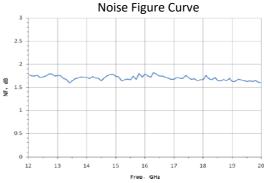
Symbol	Parameter	Test Conditions	Value			Unit
			Min	Typical	Max	
G	Gain	Vd = +5V F : 12 ~ 20GHz	28	29	ı	dB
NF	Noise Figure		-	1.5	2	dB
Id	Static Current		-	65	•	mA
VSWR_in	Input Standing Wave		-	1.5	•	-
VSWR_out	Output Standing Wave		-	1.5	-	-
P-1	Output Power at 1dB point		-	8	-	dBm

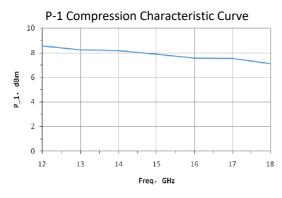
Typical Performance



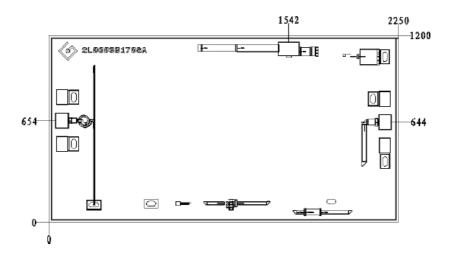




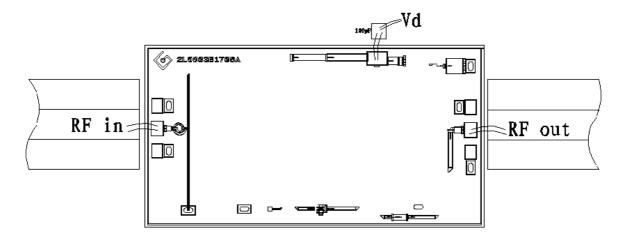




Chip Dimensions (Unit: μ m)



Chip Layout Diagram



Pad Definition

Symbol	Function	Dimension	Equivalent Circuit
RF_in	RF signal input port, connecting to external 50Ω system. no need to add DC blocking capacitor.	100*100μm²	RF-in
RF_out	RF signal output port, connecting to external 50Ω system, no need to add DC blocking capacitor.	100*100μm²	RF_out
Vd	Amplifier bias, need to connect external 100pF capacitor.	100*100μm²	AD Comment of the second of th

Please see Appendix A for details.