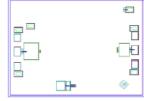
AMT1209 7 – 13GHz Low Noise Amplifier Chip



Key Features :

- Frequency range : 7 13GHz
- Typical gain : 10dB
- Input standing wave : 1.5
- Output standing wave : 1.5
- Noise figure : 2.3dB
- P-1 : 12dBm @ +4.1V/28mA
- Chip dimensions : 1.5mm x 1.1mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

Description:

AMT1209 chip is a Gallium Arsenide (GaAs) high performance Low Noise Amplifier, it covers 7 – 13GHz frequency range. It uses +4.1V single voltage operation, Noise Figure is 2.3dB, and 10dB typical gain. This chip is designed with ground through metal vias on the back technology. All chip products p are 100% RF tested.

Absolute Maximum Ratings (Ta = 25°C)

Symbol	Parameter	Value	Remark
Vd	Drain Voltage	7V	
Pin	Input Signal Power	17dBm	
Tch	Operating Temperature	150°C	
Tm	Sintering Temperature	310°C	30s, N ₂ protection
Tstg	Storage Temperature	-65 ~ +150°C	

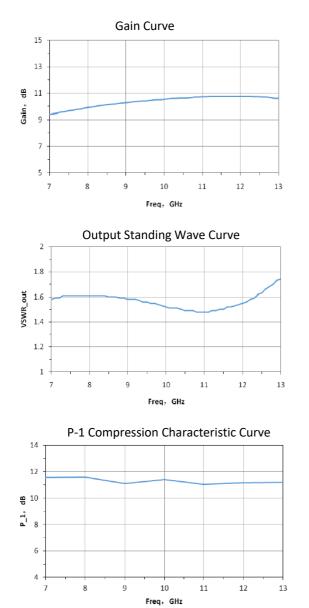
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

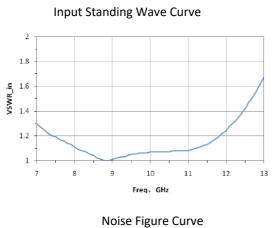
Electrical Characteristics (Ta = 25°C)

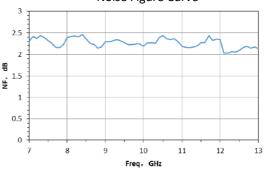
Symbol	Parameter	Test Conditions	Value			Unit
			Min	Typical	Max	
G	Gain		9.5	10	-	dB
NF	Noise Figure		-	2.3	2.4	dB
Id	Static Current	Vd = +4.1V	-	28	-	mA
VSWR_in	Input Standing Wave	F : 7 ~ 13GHz	-	1.5	-	
VSWR_out	Output Standing Wave		-	1.5	-	
P-1	Output Power at 1dB point		-	12	-	dBm

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Typical Performance

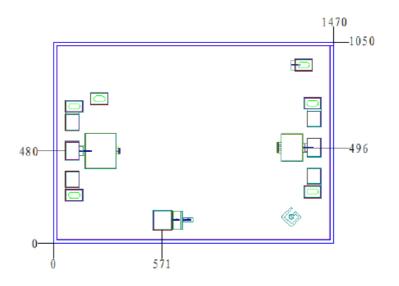




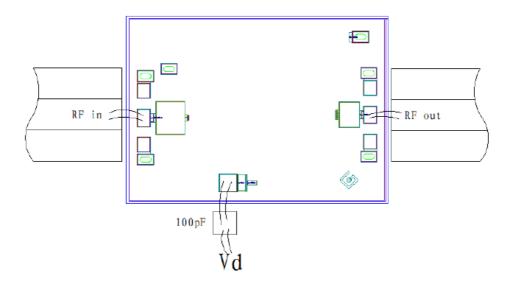


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Chip Dimensions (Unit : µm)



Chip Layout Diagram



Pad Definition

Symbol	Function	Dimension	Equivalent Circuit
RF_in	RF signal input port, connecting to external 50 Ω system. no need to add DC blocking capacitor.	100*100µm²	RF₋in ↔
RF_out	RF signal output port, connecting to external 50 Ω system, no need to add DC blocking capacitor.	100*100µm²	-L-H-O RF_out
Vd	Amplifier bias, need to connect external 100pF capacitor.	100*100μm²	

Please see Appendix A for details.

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