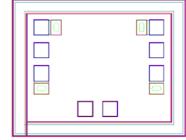
AMT1612 0 - 20GHz Digital Attenuator Chip



Key Features :

- Frequency range : 0 20GHz
- Insertion loss : 2dB
- Attenuation : 32dB
- Input/output standing wave : 1.4/1.4
- Control method : 0V/-5V
- Chip dimensions : 1.05mm x 0.8mm x 0.1mm
- Applications : wireless communication, transceiver module, radio telecommunication etc.

Description:

AMT1612 is a one-bit 32dB digital control attenuator, it is designed by Gallium Arsenide (GaAs) process. This chip is designed with ground through metal vias on the back technology, it covers a frequency range of $0 \sim 20$ GHz, typical insertion loss is 2dB, and uses 0V/-5V control. This chip is for microwave transceiver module, to realize transceiver signal amplitude control function.

Absolute Maximum Ratings (Ta = 25°C)

| Symbol | Parameter | Value | Remark | |
|--------|-----------------------|--------------|--------------------------------|--|
| Pin | Input Power | 25dBm | | |
| Tch | Operation Temperature | -55 ~ +125°C | | |
| Tm | Sintering Temperature | 310°C | 30s, N ₂ protection | |
| Tstg | Storage Temperature | -65 ~ +150°C | | |

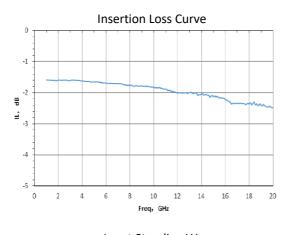
[1] Operation outside any of the Absolute Maximum Ratings may cause permanent device damage.

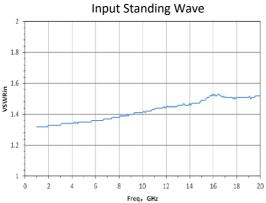
Electrical Characteristics (Ta = 25°C)

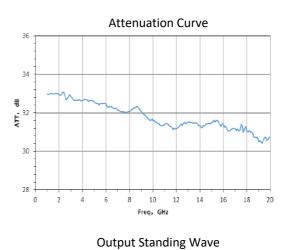
| Symbol | Parameter | Test Conditions | Value | | Unit | |
|---------|----------------------|-----------------|-------|---------|------|----|
| | | | Min | Typical | Max | |
| IL | Insertion Loss | | - | 2 | 2.3 | dB |
| ATT | Attenuation | | 30.7 | 32 | 33.3 | dB |
| VSWRin | Input Standing Wave | F : 0 ~ 20GHz | - | 1.4 | 1.5 | - |
| VSWRout | Output Standing Wave | | - | 1.4 | 1.5 | - |

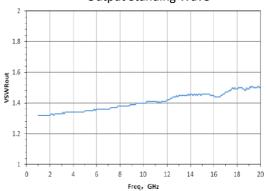
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Typical Performance



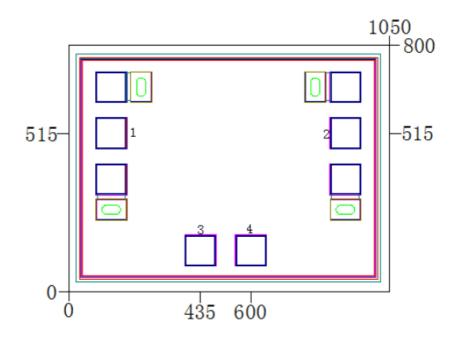




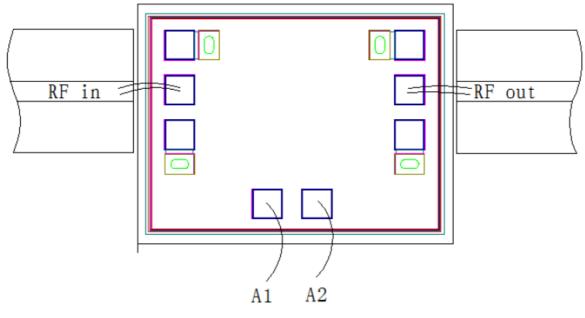


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Chip Dimensions (Unit : µm)



Chip Layout Diagram



Pad Definition

| Symbol | Function | Dimension | |
|--------|---|-------------|--|
| RFin | RF signal input port, external connect to 50 Ω system, no DC blocking capacitor | 100µm*100µm | |
| RFout | RF signal output port, external connect to 50 Ω system, no DC blocking capacitor | 100µm*100µm | |
| A1 | Input 0V/-5V separately to A1, A2, attenuator at initial state | 100µm*100µm | |
| A2 | Input -5V/0V separately to A1, A2, attenuator at attenuating state | 100µm*100µm | |

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